

**GUIDE TO RISC PROCESSORS: FOR PROGRAMMERS
AND ENGINEERS**

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In the last chapter, we presented many details on the processor design space as well as the. CISC and RISC architectures. It is time we consolidated our.

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We can use `addc`. There is no remainder computed. Logical Instructions The three basic logical operations—`and`, `or`, and `xor`—are supported. Space for remaining arguments 7,8. So there was a need to minimize the amount of memory required to store a program. For example, the `addi` instruction, described in the next section, uses this format.

The right bit gives the actual result of the branch instruction. The `IATendst` comparing, the values are treated as signed integers. This type of code is called relocatable code.